Christopher D. Walters

Phoenix AZ 85281 chrisw@edawizardry.com

PROFESSIONAL OBJECTIVE

To pursue a challenging career in software development that allows room for continued growth.

SKILLSET

PCB design flow EDA (Cadence PSD toolsuite) Programmer/Analyst

Webapp design DBA

Programming Languages

Functionality

Cadence SKILL C++ (VC++6.0 on Windows, g++ on *nix) VB6.0 on Windows Dojo/Ajax/Java/JSP/servlets Spring/Maven/Java/Eclipse Salesforce API

Perl shell scripting (*nix) SQL Python

EXPERIENCE

02/2016 – 10/2017 QA Automation Team Lead Intel

Duties focused on leading a small team in developing and maintaining a rigorous automated QA process for validating, comparing and regression-testing of Process Development Kits (PDKs) based on Ruby, Python, shell scripting and SKILL using custom tools and Cadence Virtuoso. Time was split evenly between implementing new tests and mentoring junior developers in the use of the process, tools and establishing and achieving career goals through developing action plans.

09/2014 - 08/2015 QA Automation Consultant

Intel

Duties focused on developing and maintaining a rigorous automated QA process for validating, comparing and regression-testing of Foundry Development Kits (FDKs) based on Perl, Python, Scheme and SKILL using open-source tools and Cadence Virtuoso.

06/2013 – 08/2014 Team Lead, CRM conversions AIG

Used a complex series of SQL queries to extract data from several commercial CRM tools into a common Access database structure, followed by Perl scripts to format and validate the date at the field level, concluding with a Java app using the Salesforce API to load the data into a customer's Salesforce client.

04/2011 – 2/2013 Webapp Developer

CWIE Holdings, Inc. Used Spring/Maven/Java/MySQL/Eclipse components to create dynamic web applications for content download and customer billing.

01/2010 – 06/2010 Webapp Developer Gilbert Public School District Used Eclipse/JSP/JSF/Hibernate/MySQL components to create dynamic web applications in a mixed PC/Linux environment for both external and internal customers.

02/2008 – 05/2008 Database Consultant Avnet

Duties focused on error-checking and aggregating data from 60+ spreadsheets into several MySQL tables using a custom Visual Basic application, Perl script and MySQL routine.

07/2007 - 09/2007

Automation Consultant

Harbor Electronics

Duties focused exclusively on developing a series of post-design error-checking reports and a GUI interface to allow selection of which reports to execute and the report results.

09/2006 - 12/2006

Automation Consultant

Harbor Electronics

Duties focused exclusively on developing a process for extracting trace-length data from a design and loading the data into a spreadsheet. This process called for a series of SKILL routines on the Allegro side and a VB application using OLE automation to populate the spreadsheet.

07/2004 - 05/2006

Sr. Design Automation Engineer, Engineering EDA Tool Support Apple

Duties focused primarily on developing, integrating and maintaining custom design automation software in SKILL and Perl that improves the PCB design workflow in conjunction with the Cadence schematic capture, PCB layout and routing software tools (Concept, Allegro, SPECCTRA) with heavy emphasis on converting series of replay-scripts called from the command line into SKILL routines with GUI front-ends for improved ease-of-use and flow-control. Several routines were developed to convert 3rd-party designs to use in-house PCB symbols.

04/2001 - 02/2004

Sr. Design Automation Engineer, Engineering EDA Tool Support Nvidia

Duties focused primarily on developing, integrating and maintaining custom design automation software in SKILL, C++, and Perl that improves the PCB design work flow in conjunction with the Cadence PSD toolsuite while continuing to provide direct user support and toolsuite maintenance. Integral to this was using an API to an interface a component-management database into creating Concept symbol parameter files.

04/1987 - 03/2001

Staff Engineer, Engineering EDA Tools Support,

Motorola Government Electronics Group

Duties consisted of supporting and maintaining the schematic capture, PCB layout and routing software tools (Concept, Allegro, Specctra), providing user support and design consultation, installation and testing of new versions of the tools and maintenance of standard symbol & footprint libraries across project and divisional boundaries as well as developing and maintaining custom design automation software.

EDUCATION

09/1982 – 08/1986 BSEE, Montana State University, Bozeman MT